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Solid-State Circuits, IEEE Journal of

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2. A 900 MHz integrated discrete-time filtering RF front-end

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Solid-State Circuits Conference, 1996. Digest of Technical Papers. 43rd ISSC(

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Weide, G.; Waite, H.; Yifeng Zhang; Aggarwal, S.; Razzell, C.;

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2. Fixed-point error analysis of two DCT algorithms

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1. Comments on "Nonparametric detection"

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Lytle, D.W.; Moose, P.H.; Scharf, L.L.;

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1. Carrier frequency offset estimation for OFDM-based WLANs

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Jian Li; Guoqing Liu; Giannakis, G.B.;

Signal Processing Letters, IEEE

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